

Amendments to the Specification

Please replace the paragraph beginning on page 6, line 2 with the following amended paragraph:

Sources of the ~~reference potential side resistors R1~~ PMOS transistors P1 to P7 are connected to the reference potential terminal Vref . Drains of the ~~reference potential side resistors R1~~ PMOS transistors P1 to P7 are connected to the output terminal OUT through the amplifier AMP. The gates of the ~~reference potential side resistances R1~~ PMOS transistors P1 to P7 are connected to a decoder circuit 20, which will be described later. The reference potential side resistors R1 have equal resistance values.

Please replace the paragraph beginning on page 6, line 10 with the following amended paragraph:

The PMOS transistors P1 to P7 function as switches for switching paths ~~can be selected to select~~ between the reference potential terminal Vref and the reference potential side resistors R1 and for changing the number of the reference potential side resistors R1 from one to seven between the reference potential terminal Vref and the output terminal OUT. In other words, one of the PMOS transistors P1 to P7 is turned on so that one of the paths between the reference potential terminal Vref and the output terminal OUT ~~can be selected~~. Thus, the number of the reference potential side resistors R1 between the reference potential terminal Vref and the output terminal OUT is changed from one to seven.

**Please replace the paragraph beginning on page 6, line 23 with the
following amended paragraph:**

Sources of the ~~ground potential side resistors R2 NMOS transistors N0 to N7~~ are connected to the ground potential terminal GND. Drains of the ~~ground potential side resistors R2 NMOS transistors N0 to N7~~ are connected to the output terminal OUT through the amplifier AMP. The gates of the ~~ground potential side resistors R2 NMOS transistors N0 to N7~~ are connected to the decoder circuit 20, which will be described later. The ground potential side resistors R2 have equal resistance values. The resistance values are equal to those of the reference potential side resistors R1.